MARKED-UP AMENDED DRAWING

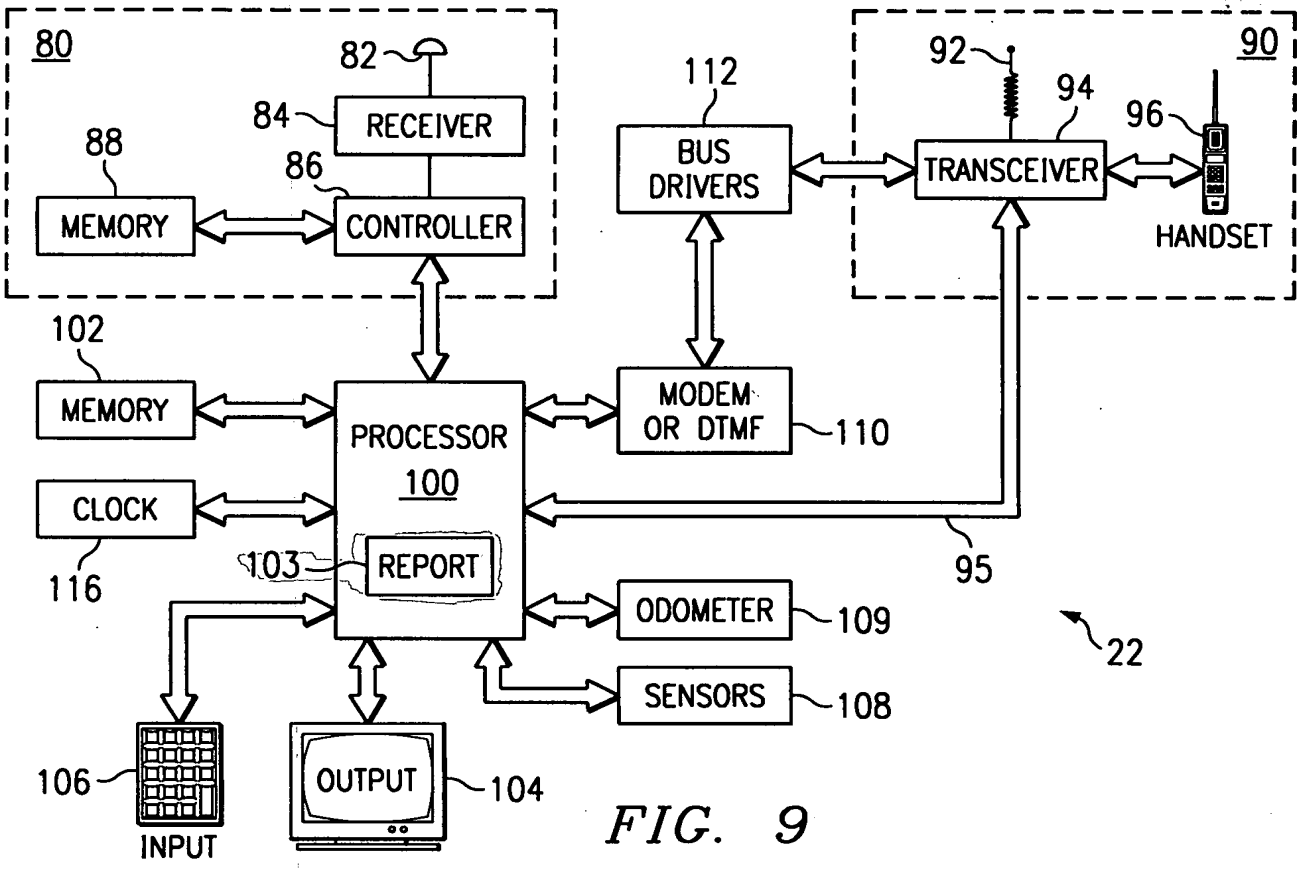

80
82
RECEIVER
84
88
86
MEMORY
CONTROLLER
112
BUS
DRIVERS
92
90
94
96
TRANSCEIVER
HANDSET
102
MEMORY
PROCESSOR
100
MODEM
OR DTMF
110
CLOCK
116
103
REPORT
95
ODOMETER
109
22
SENSORS
108
106
INPUT
OUTPUT
104

FIG. 9

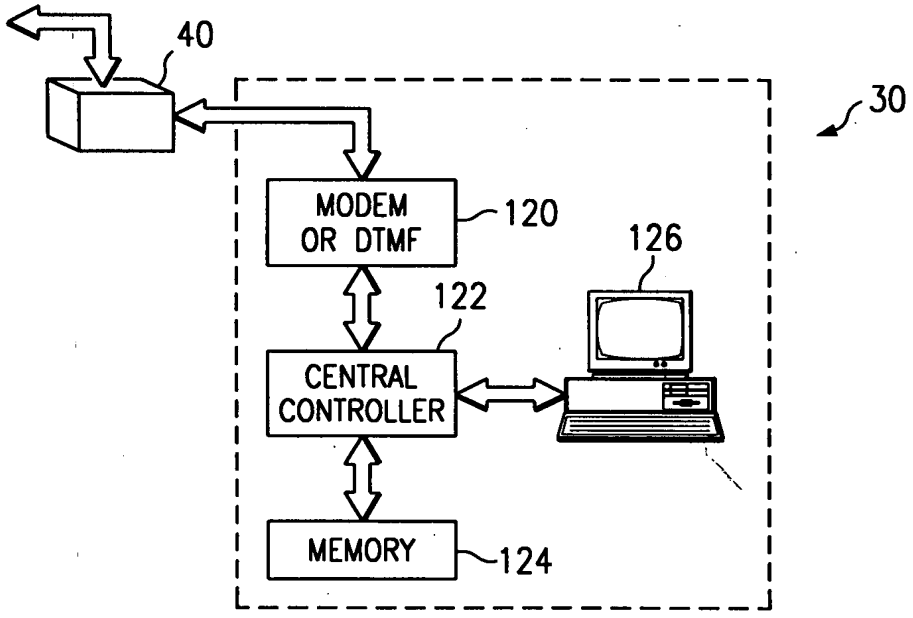
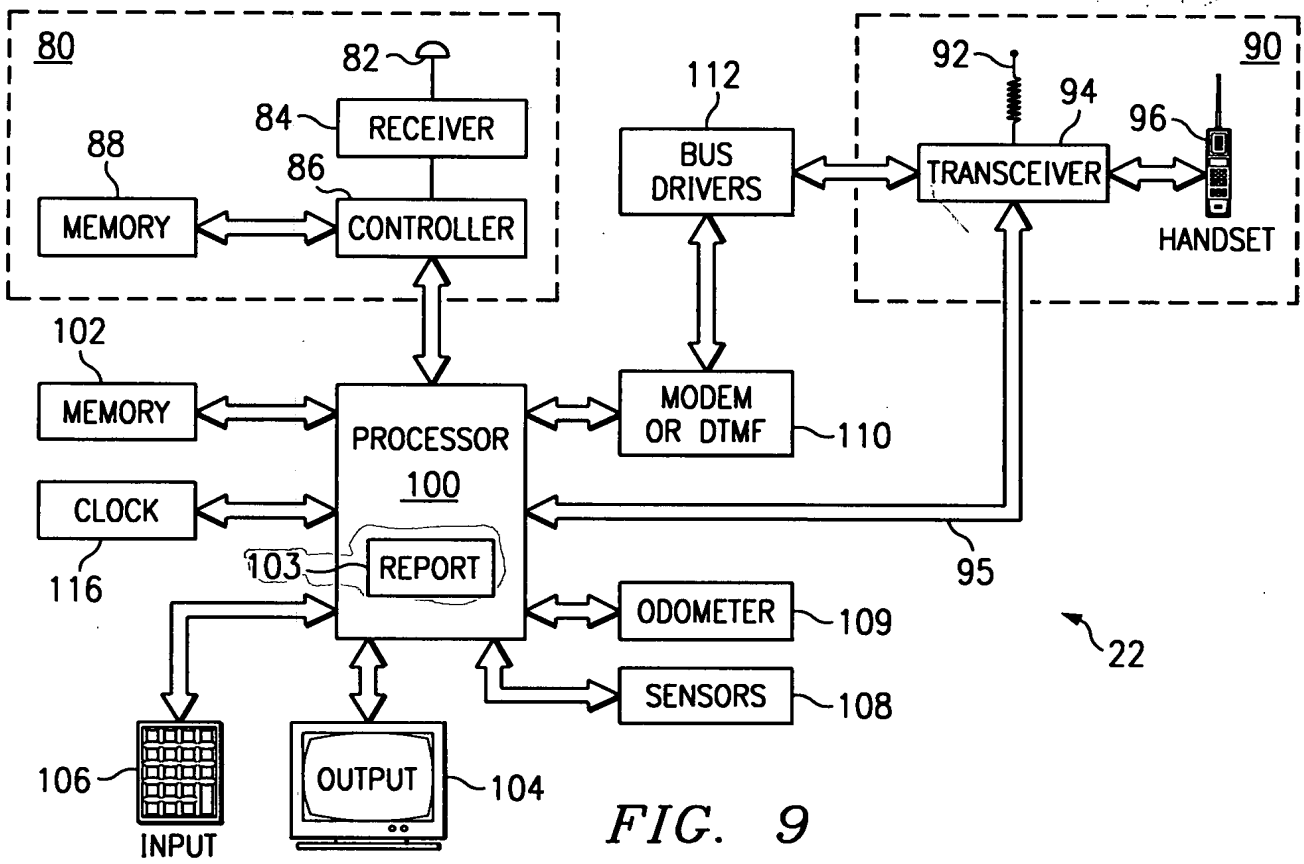

40
30
MODEM
OR DTMF
120
126
122
CENTRAL
CONTROLLER
MEMORY
124

FIG. 10


80
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84
88
86
MEMORY
CONTROLLER
112
BUS DRIVERS
92
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96
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HANDSET
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MODEM OR DTMF
110
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REPORT
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ODOMETER
109
22
SENSORS
108
106
INPUT
OUTPUT
104

FIG. 9

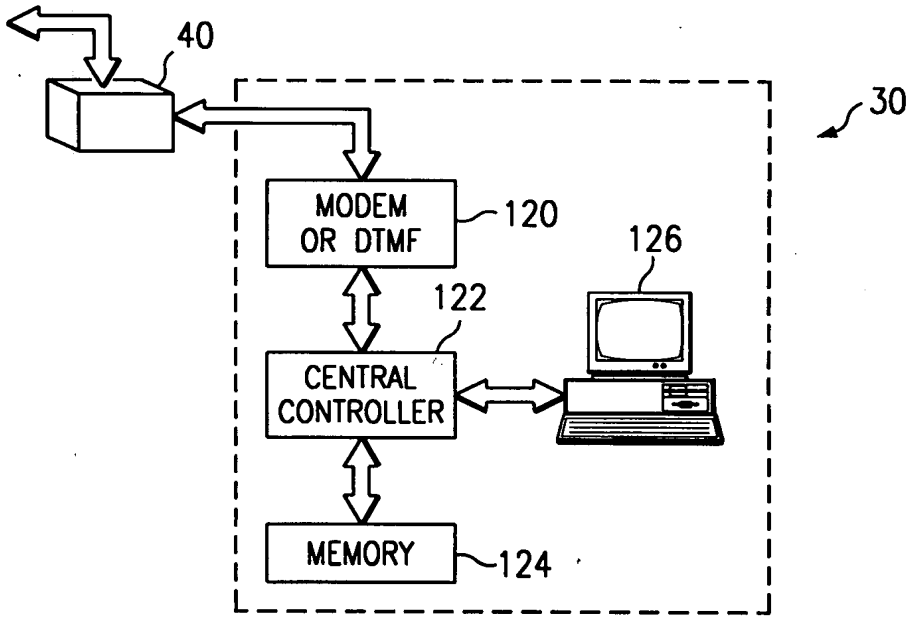
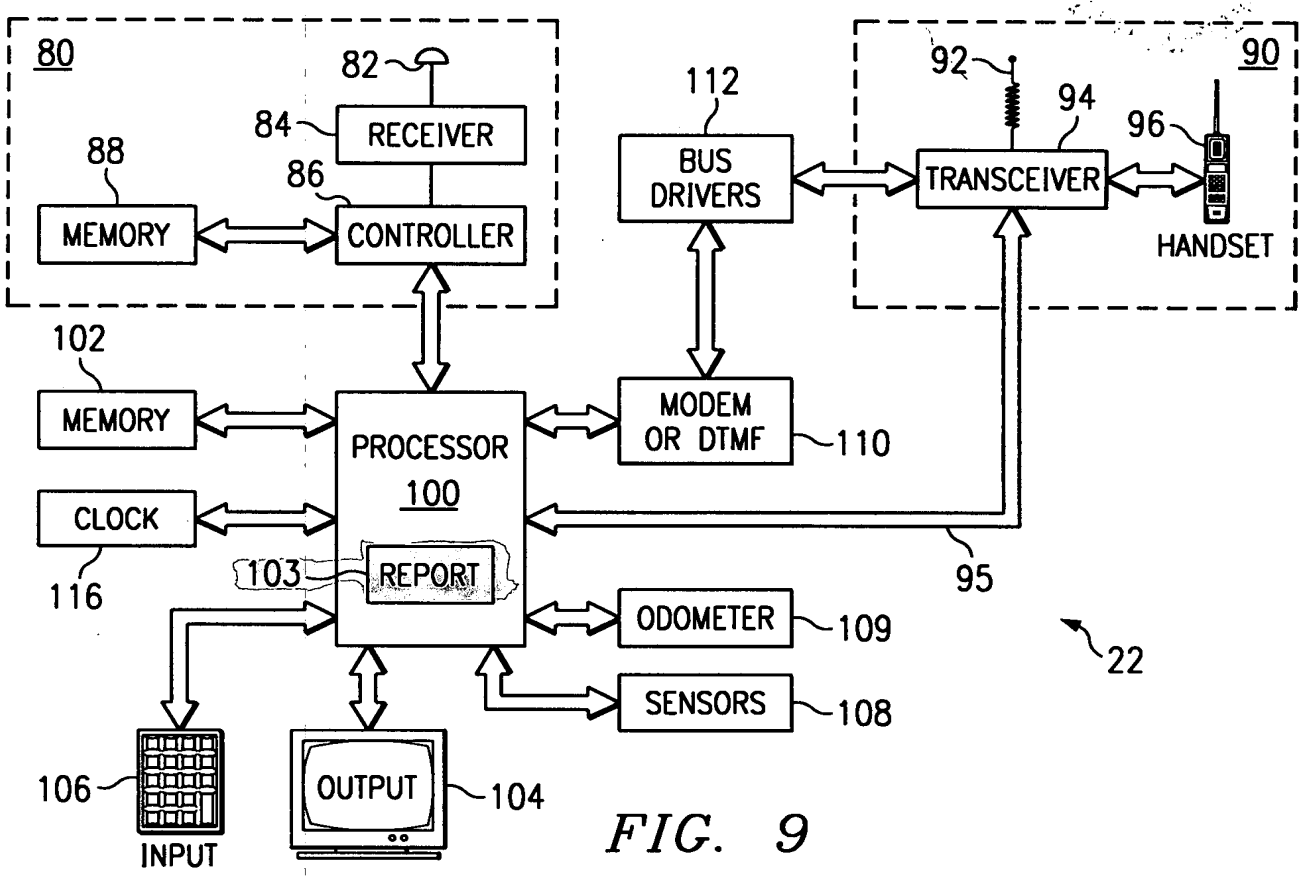

40
30
MODEM OR DTMF
120
126
122
CENTRAL CONTROLLER
MEMORY
124

FIG. 10


80
82
RECEIVER
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MEMORY
CONTROLLER
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BUS DRIVERS
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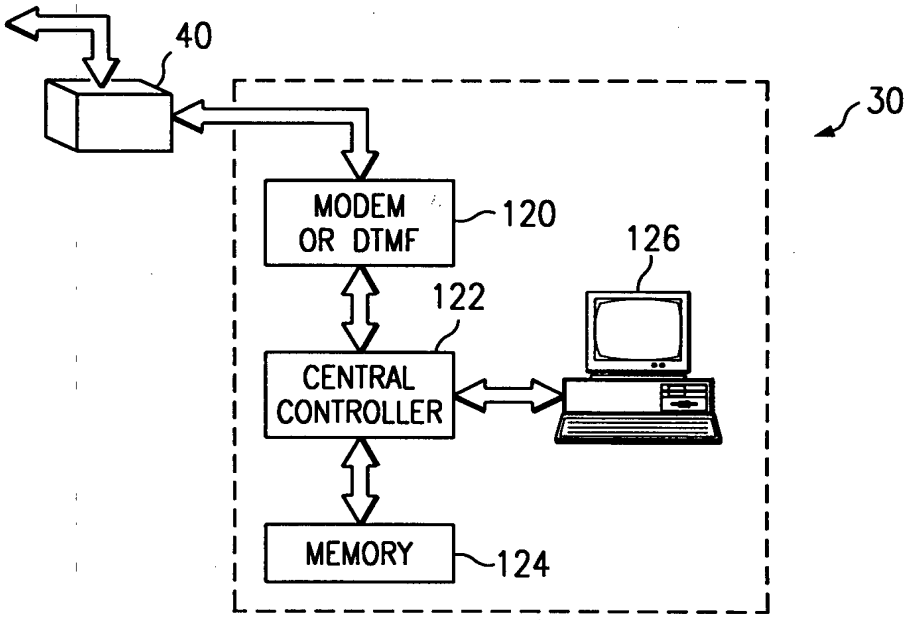
FIG. 9


40
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MODEM OR DTMF
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FIG. 10